

4V Drive Nch MOSFET

RSF014N03

●Structure

Silicon N-channel MOSFET

●Features

- 1) Low On-resistance.
- 2) Space saving, small surface mount package (TUMT3).
- 3) 4V drive.

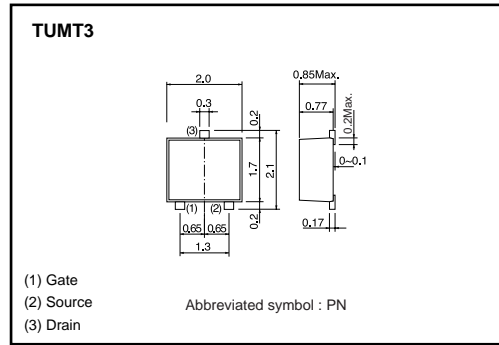
●Applications

Switching

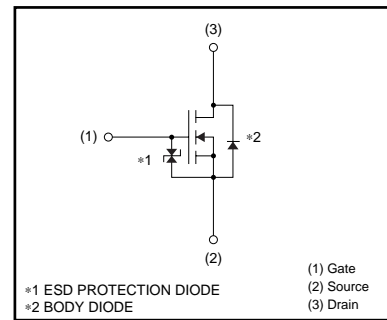
●Packaging specifications

Type	Package	Taping
	Code	TL
	Basic ordering unit (pieces)	3000
RSF014N03		○

●Dimensions (Unit : mm)



●Inner circuit



●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Drain-source voltage	V _{DSS}	30	V
Gate-source voltage	V _{GSS}	20	V
Drain current	Continuous	I _D	±1.4 A
	Pulsed	I _{DP} *1	±5.6 A
Source current (Body diode)	Continuous	I _S	0.6 A
	Pulsed	I _{SP} *1	5.6 A
Total power dissipation	P _D *2	0.8	W
Channel temperature	T _{ch}	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C

*1 Pw≤10μs, Duty cycle≤1%
*2 Mounted on a ceramic board

●Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	R _{th(ch-a)} *	156	°C/W

* Mounted on a ceramic board

Transistors

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Gate-source leakage	I_{GSS}	–	–	10	μA	$V_{GS}=20V, V_{DS}=0V$
Drain-source breakdown voltage	$V_{(BR)DSS}$	30	–	–	V	$I_D=1mA, V_{GS}=0V$
Zero gate voltage drain current	I_{DSS}	–	–	1	μA	$V_{DS}=30V, V_{GS}=0V$
Gate threshold voltage	$V_{GS(th)}$	1.0	–	2.5	V	$V_{DS}=10V, I_D=1mA$
Static drain-source on-state resistance	$R_{DS(on)}$ *	–	170	240	$m\Omega$	$I_D=1.4A, V_{GS}=10V$
		–	250	350	$m\Omega$	$I_D=1.4A, V_{GS}=4.5V$
		–	270	380	$m\Omega$	$I_D=1.4A, V_{GS}=4V$
Forward transfer admittance	$ Y_{fs} $ *	1	–	–	S	$V_{DS}=10V, I_D=1.4A$
Input capacitance	C_{iss}	–	70	–	pF	$V_{DS}=10V$
Output capacitance	C_{oss}	–	15	–	pF	$V_{GS}=0V$
Reverse transfer capacitance	C_{rss}	–	12	–	pF	$f=1MHz$
Turn-on delay time	$t_{d(on)}$ *	–	6	–	ns	$V_{DD}\doteq 15V$
Rise time	t_r *	–	6	–	ns	$I_D=0.7A$
Turn-off delay time	$t_{d(off)}$ *	–	13	–	ns	$V_{GS}=10V$
Fall time	t_f *	–	8	–	ns	$R_L=21\Omega$ $R_G=10\Omega$
Total gate charge	Q_g *	–	1.4	2.0	nC	$V_{DD}\doteq 15V$ $R_L=11\Omega$
Gate-source charge	Q_{gs} *	–	0.6	–	nC	$V_{GS}=5V$ $R_G=10\Omega$
Gate-drain charge	Q_{gd} *	–	0.3	–	nC	$I_D=1.4A$

*Pulsed

●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	V_{SD}	–	–	1.2	V	$I_S=0.6A, V_{GS}=0V$

Transistors

●Electrical characteristics curves

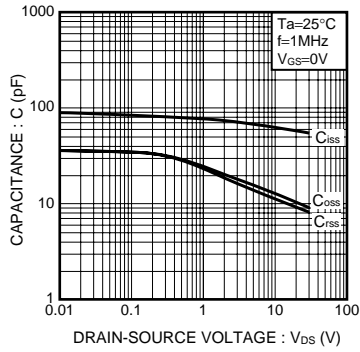


Fig.1 Typical Capacitance vs. Drain-Source Voltage

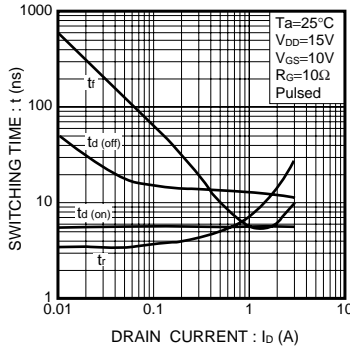


Fig.2 Switching Characteristics

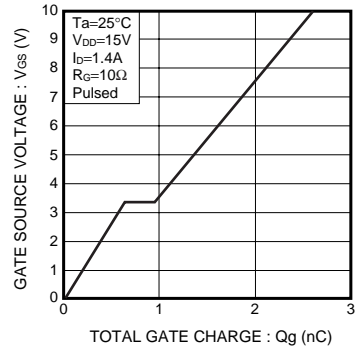


Fig.3 Dynamic Input Characteristics

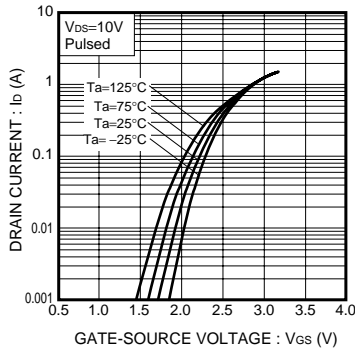


Fig.4 Typical Transfer Characteristics

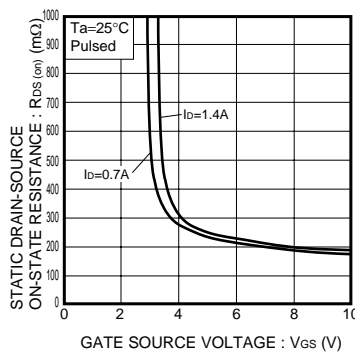


Fig.5 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

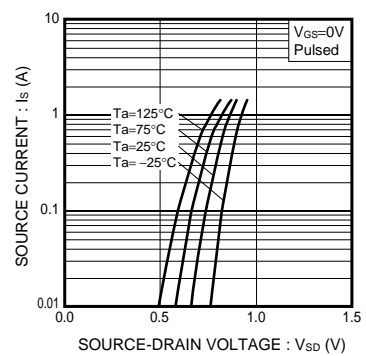


Fig.6 Source Current vs. Source-Drain Voltage

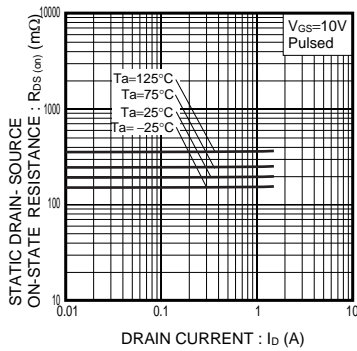


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current (I)

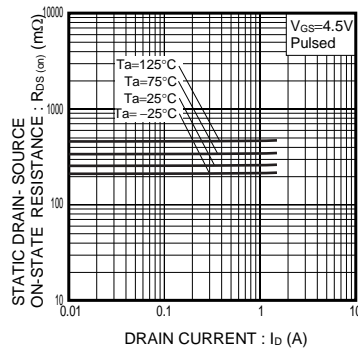


Fig.8 Static Drain-Source On-State Resistance vs. Drain Current (II)

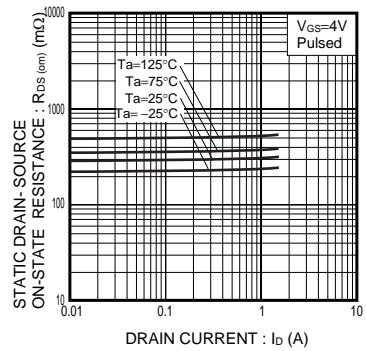


Fig.9 Static Drain-Source On-State Resistance vs. Drain Current (III)

Transistors

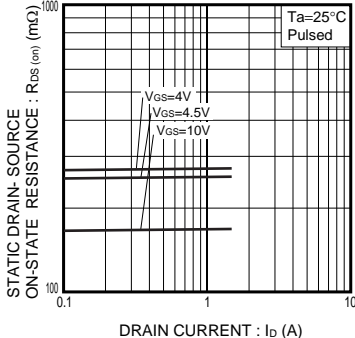


Fig.10 Static Drain-Source On-State Resistance vs. Drain Current (IV)

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